

IN THE CLAIMS:

Please amend the claims as follows.

1. (Currently Amended) Method for identifying a defective plug-in unit in a system comprising:

a first bus (~~PCI~~);

an interface circuit (~~1~~) provided with a first register (~~A~~) and a second register (~~B~~);

at least two plug-in units (~~2~~) connected via interface circuits (~~1~~) to the first bus (~~PCI~~);

a second bus (~~3~~) connected to at least one plug-in unit (~~2¹~~); and

an operation and maintenance facility (~~4~~) connected to the second bus (~~3~~); and

in which method ~~the~~ a first plug-in unit (~~2¹~~) of the at least two plug-in units
addresses ~~the~~ a second plug-in unit (~~2²~~) of the at least two plug-in units with a bus
address, wherein

the bus address is transferred into the first register (~~A~~); and

the bus address is transferred, in conjunction with a reboot, from the first register
(~~A~~) into the second register (~~B~~).

2. (Currently Amended) Method as defined in claim 1, wherein the bus address is
read from the second register (~~B~~) by means of the operation and maintenance facility (~~4~~).

3. (Currently Amended) Method as defined in claim 1, wherein the first bus (~~PCI~~) is disposed in a CompactPCI bus.

4. (Currently Amended) System for identifying a defective plug-in unit, said system comprising:

a first bus (~~PCI~~);
an interface circuit (~~1~~) provided with a first register (~~A~~) and a second register (~~B~~);
at least two plug-in units (~~2~~) connected via interface circuits (~~1~~) to the first bus (~~PCI~~), a first plug-in unit (~~2~~¹) comprising means for addressing a second plug-in unit (~~2~~²) with a bus address;
a second bus (~~3~~) connected to at least one plug-in unit (~~2~~¹); and
an operation and maintenance facility (~~4~~) connected to the second bus (~~3~~),

wherein the system comprises:

means for transferring the bus address into the first register (~~A~~);
means for transferring the bus address, in conjunction with a reboot, from the first register (~~A~~) into the second register (~~B~~); and
means for reading the bus address from the second register (~~B~~) by using the operation and maintenance facility (~~4~~).

5. (Currently Amended) System as defined in claim 4, wherein the first bus (~~PCI~~) is a CompactPCI bus.

6. (Currently Amended) Interface circuit ~~(1)~~, comprising:
means for ~~connected~~ connecting a first bus ~~(PCI)~~ to a plug-in unit (2);
a first register ~~(A)~~; and
a ~~subscriber~~ second register; ~~(B)~~, ~~wherein the interface circuit comprises:~~
means for transferring ~~the~~ a bus address into the first register ~~(A)~~; and
means for transferring the bus address₁ in conjunction with a reboot₁ from the first register ~~(A)~~ into the second register ~~(B)~~.

7. (Currently Amended) Interface circuit as defined in claim 6, wherein the interface circuit ~~(1)~~ comprises means for sending the bus address from the second register ~~(B)~~ to ~~the~~ an operation and maintenance facility (4).

8. (Currently Amended) System as defined in claim 6, wherein the first bus ~~(PCI)~~ is a CompactPCI bus.